## UTILITY PATENT APPLICATION TRANSMITTAL

(Only for new nonprovisional applications under 37 CFR 1.53(b))

Attorney Docket No. 241341US2S

First Inventor or Application Identifier Keiji HOSOTANI

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND METHOD OF MANUFACTURING THE SAME

Assignee Name:

Assignee Address:

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APPLICATION ELEMENTS See MPEP chapter 600 concerning utility patent appli	ADDRESS TO:	51 U.S						
Fee Transmittal Form (e.g. PTO/SB/17 (Submit an original and a duplicate for fee proces)	7) sing)	ACCO	MPANYING APPLICATION PAR	rs 8				
•		7.  Assignment	ent Papers (cover sheet & docum	ent(s))				
2. Specification Total She	eets 70	8. <b>E</b> Application	on Data Sheet. See 37 CFR 1.76					
		9. □ 37 C.F.R (when there	S. §3.73(b) Statement Pow e is an assignee) Atto	er of rney				
3. Total She	eets 30	_	ranslation Document (if applicable					
				ies of IDS tions (1)				
4. Dath or Declaration Total Pa	ges	12. 🔲 Prelimina	•					
<ul> <li>a.</li></ul>	')	i —	Ivance Serial No. Postcard					
b. Copy from a prior application (3)		14. Certified (if foreign p	Copy of Priority Document(s) (1) riority is claimed)					
i. DELETION OF INVENTOF Signed statement attached deletin the prior application, see 37 C.F.R.	15.   Applican See 37 CF	t claims small entity status. R 1.27						
1.33(b).  CD-ROM or CD-R in duplicate, large t Program (Appendix)	able or Computer	16. Other:	Request for Priority, Statement	of Relevancy				
Nucleotide and/or Amino Acid Sequer	nce Submission							
6. ☐ (if applicable, all necessary)								
a.   Computer Readable Form (CRF)								
b. Specification or Sequence Listing on								
i. ☐ CD-ROM or CD-R (2 copies); c	DΓ							
ii. Paper	ovo conies							
c. Statements verifying identity of al	<del></del>	1. 15	Van halaw					
17. If a CONTINUING APPLICATION, check appropriate box, and supply the requisite information below:  ☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) of prior application no.:								
Continuation Divisional		-iii-pait (Oii )	Group Art Unit:					
The approach the state of the s								
For CONTINUATION OR DIVISIONAL APPS only: The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 4b, is considered a part of the accompanying continuation or divisional application and is hereby incorporated by reference. The incorporation can only be relied upon when a portion has been inadvertently omitted from the submitted application parts.								
18. Amend the specification by inserting before	the first line the se	entence:						
☐ This application is a ☐ Continuation ☐ Division ☐ Continuation-in-part (CIP)								
of application Serial No. Filed on								
☐ This application claims priority of provision			Filed					
19. CORRESPONDENCE ADDRESS								
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Name:	Marvin J. Spivak	Registration No.:	24,913
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Name:	C Irvin McClelland	Registration No.:	



Docket No.

241341US2S

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR(S) Keiji HOSOTANI

SERIAL NO:

New Application

FILING DATE: Herewith

FOR:

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND METHOD OF MANUFACTURING THE

SAME

## FEE TRANSMITTAL

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

FOR	FOR NUMBER FILED		NUMBER EXTRA	RATE			CALCULATIONS		
TOTAL CLAIMS	21	-	20	=	1	х	\$18	=	\$18.00
INDEPENDENT CLAIMS	7	-	3	=	4	х	\$84	=	\$336.00
☐ MULTIPLE DEPENDENT CLAIMS (If applicable)					+	\$280	=	\$0.00	
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A duplicate copy of this sheet is enclosed.

- A check in the amount of \$1,234.00 to cover the filing fee is enclosed.
- ☐ Credit card payment form is attached to cover the filing fee in the amount of
- The Director is hereby authorized to charge any additional fees which may be required for the papers being filed herewith and for which no check or credit card payment form is enclosed herewith, or credit any overpayment to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully Submitted,

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